



Attorney Docket No.: 2003309-0021 / 10980687-1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Staton, *et al.*

Examiner: Allen

Serial No.: 09/415,015

Art Unit: 2878

Filing Date: October 7, 1999

Title: "HIGH QUANTUM EFFICIENCY POINT LIGHT DETECTOR"

Assistant Commissioner for Patents
Washington, DC 20231

Sir:

AMENDMENT

In response to the Office Action mailed May 23, 2002, please amend the application as follows:

In the Drawing:

Please amend the drawing as shown in red on the accompanying paper.

In the specification:

Please replace the paragraph beginning on line 20 of page 3 with:

- If multiple capacitors are used for charge to be stored in and read out from while subsequent charge collection processes are in progress, these capacitors may be arranged in a pipeline fashion as shown in Fig. 3. A first charge is generated in a light-sensitive area 30 and then moved to a first capacitor/readout unit 31. The capacitor/readout unit 31 includes a capacitor 31a and a voltage sampling circuitry 31b for sampling the voltage across the capacitor 31a. While a second charge is generated in the light-sensitive area 30, the first charge is read out and cycled back as often as desired. Once the second charge has been generated, the first charge is either dumped or moved into a second capacitor/readout unit 32, while the second charge is moved to the first capacitor/readout unit 31 after which a third charge can be generated in the photo-sensitive area 30. The capacitor/readout unit 32, like the capacitor/readout unit 31, includes a capacitor 32a and circuitry 32b for sampling the voltage across the capacitor 32a. All